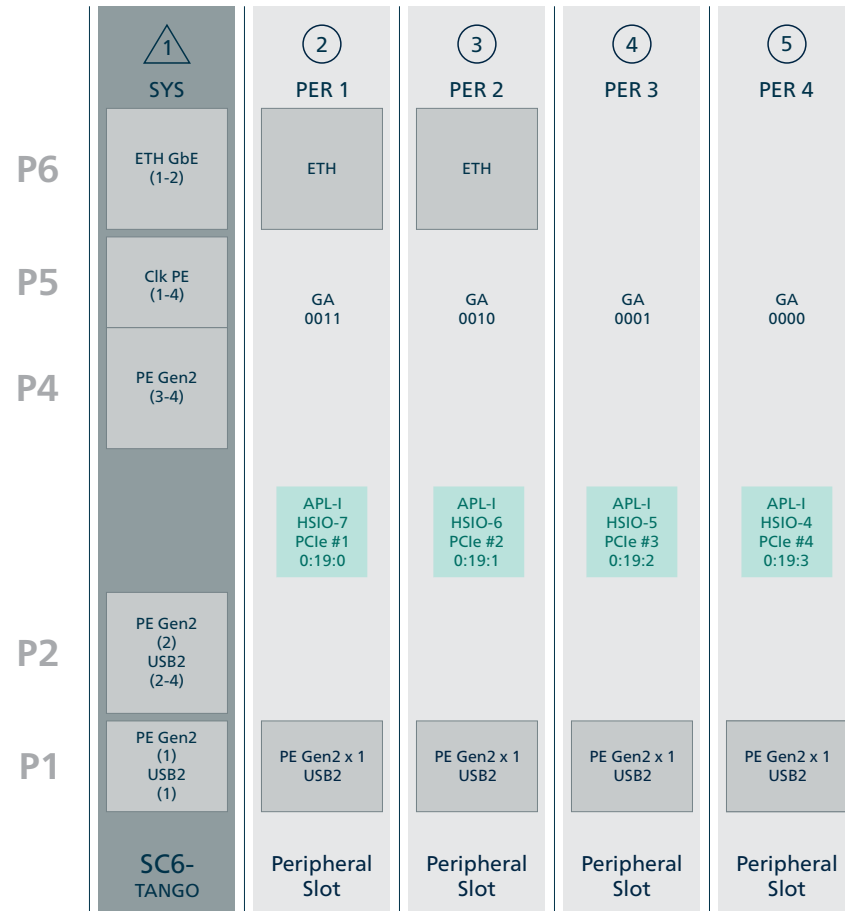
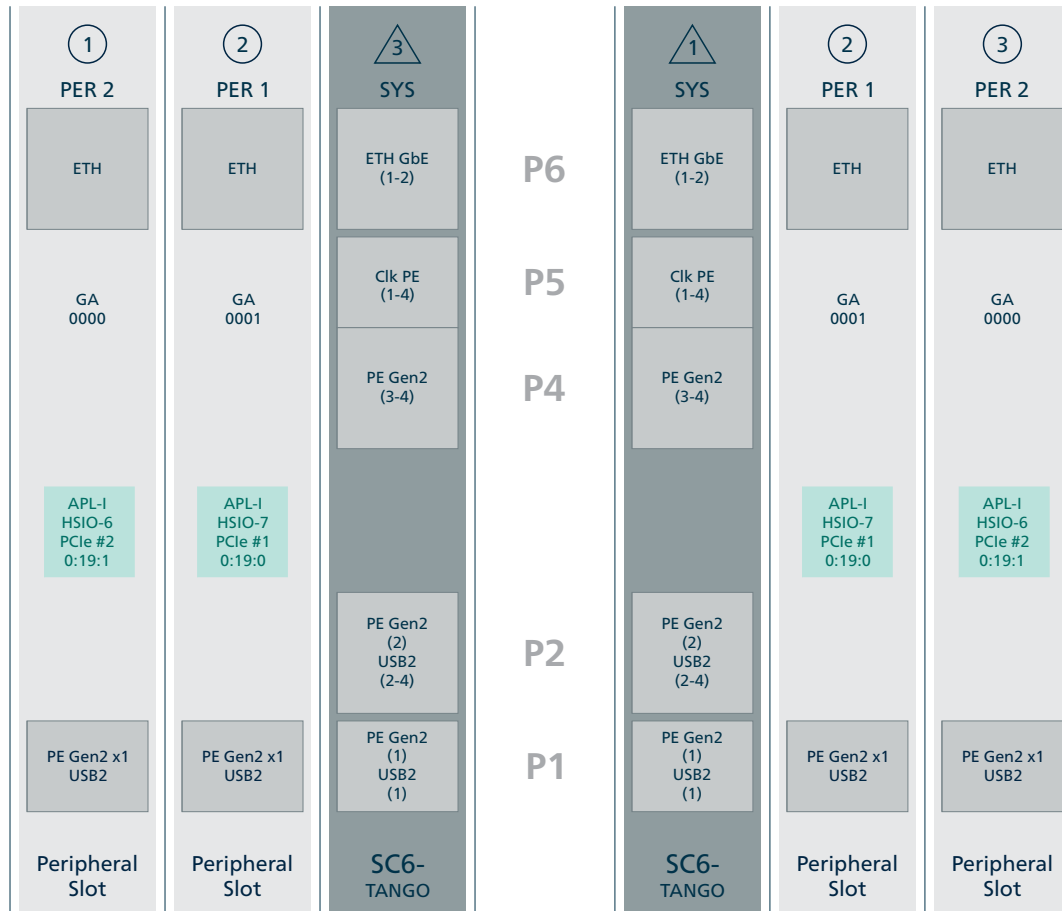


1+4 Slots backplane resources (system slot right)
 Peripheral slot target in brackets
 Pin assignment according to CPCI-5.0 specification



1+4 Slots backplane resources (system slot left)
 Peripheral slot target in brackets
 Pin assignment according to CPCI-5.0 specification



1+2 Slots backplane resources (system slot right)
 Peripheral slot target in brackets
 Pin assignment according to CPCI-S.0 specification

1+2 Slots backplane resources (system slot left)
 Peripheral slot target in brackets
 Pin assignment according to CPCI-S.0 specification